

200220" E496200T

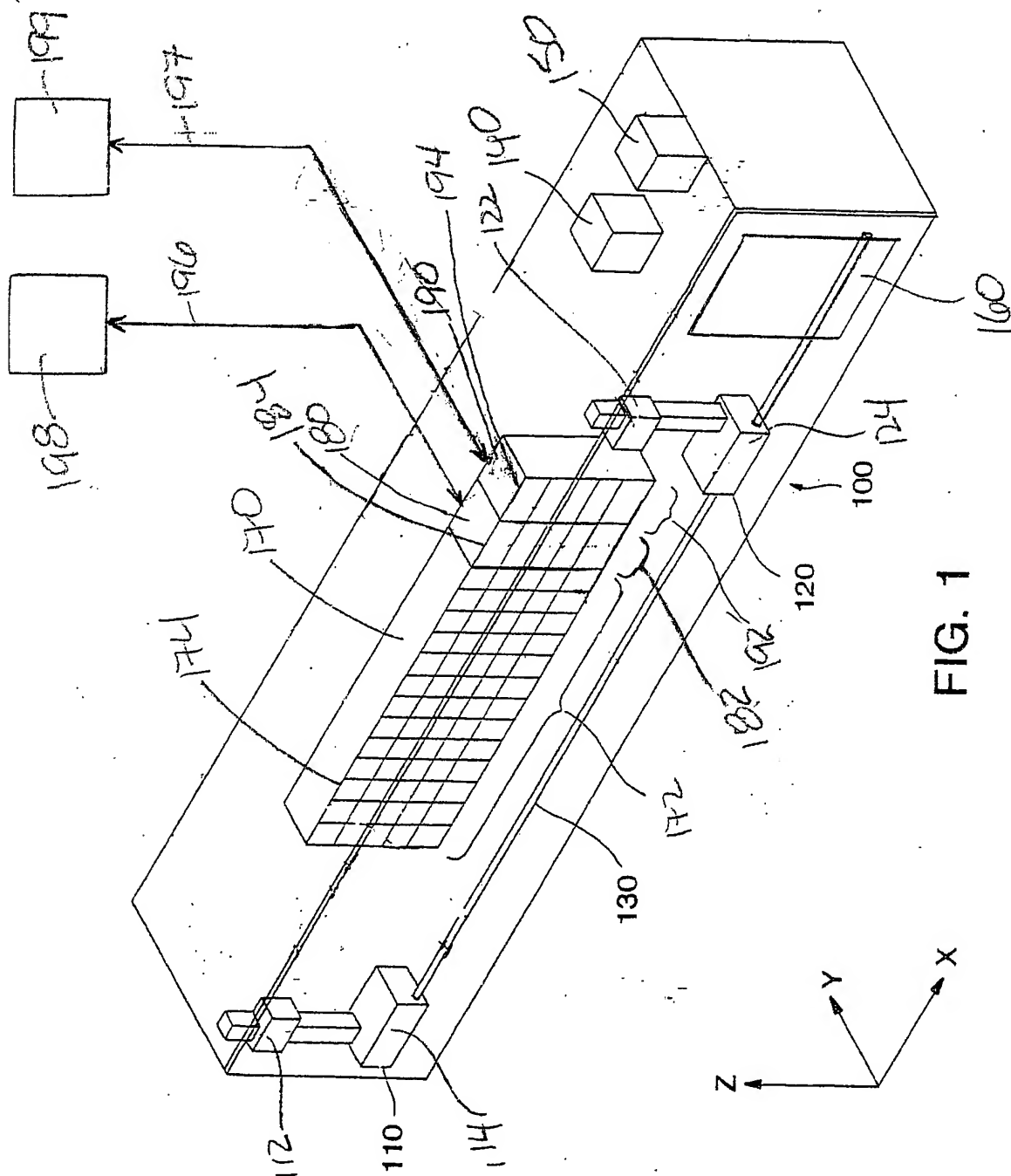


FIG. 1

FIG. 2A

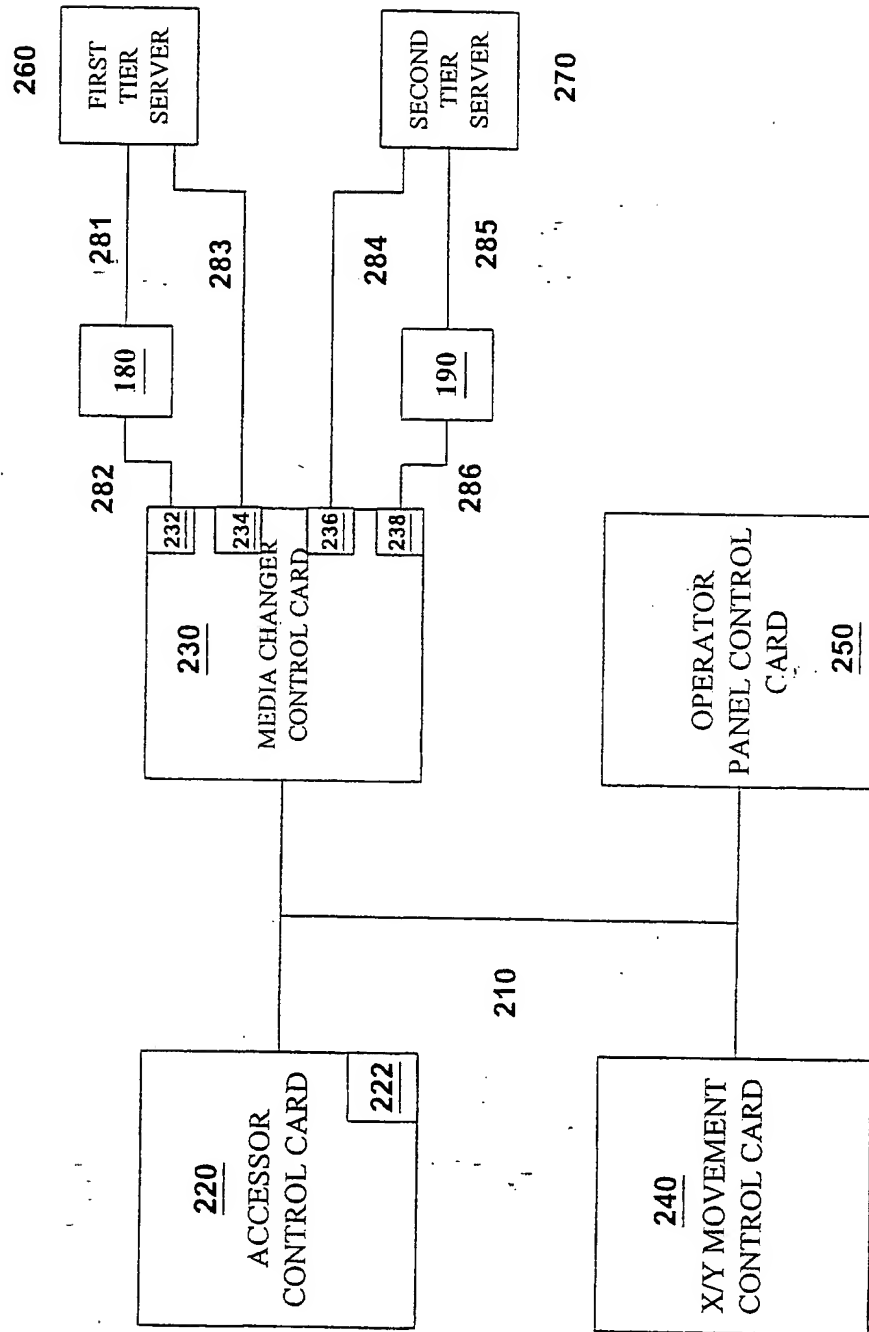


FIG. 2B

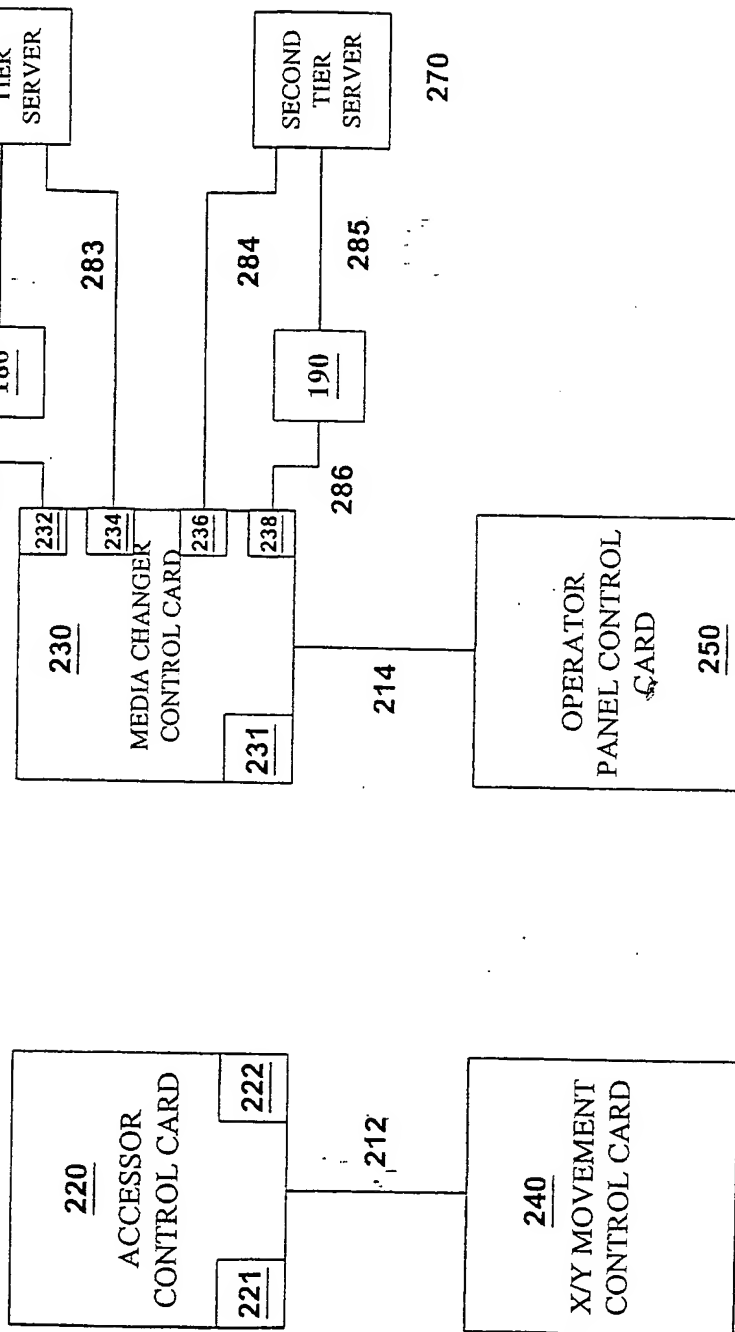
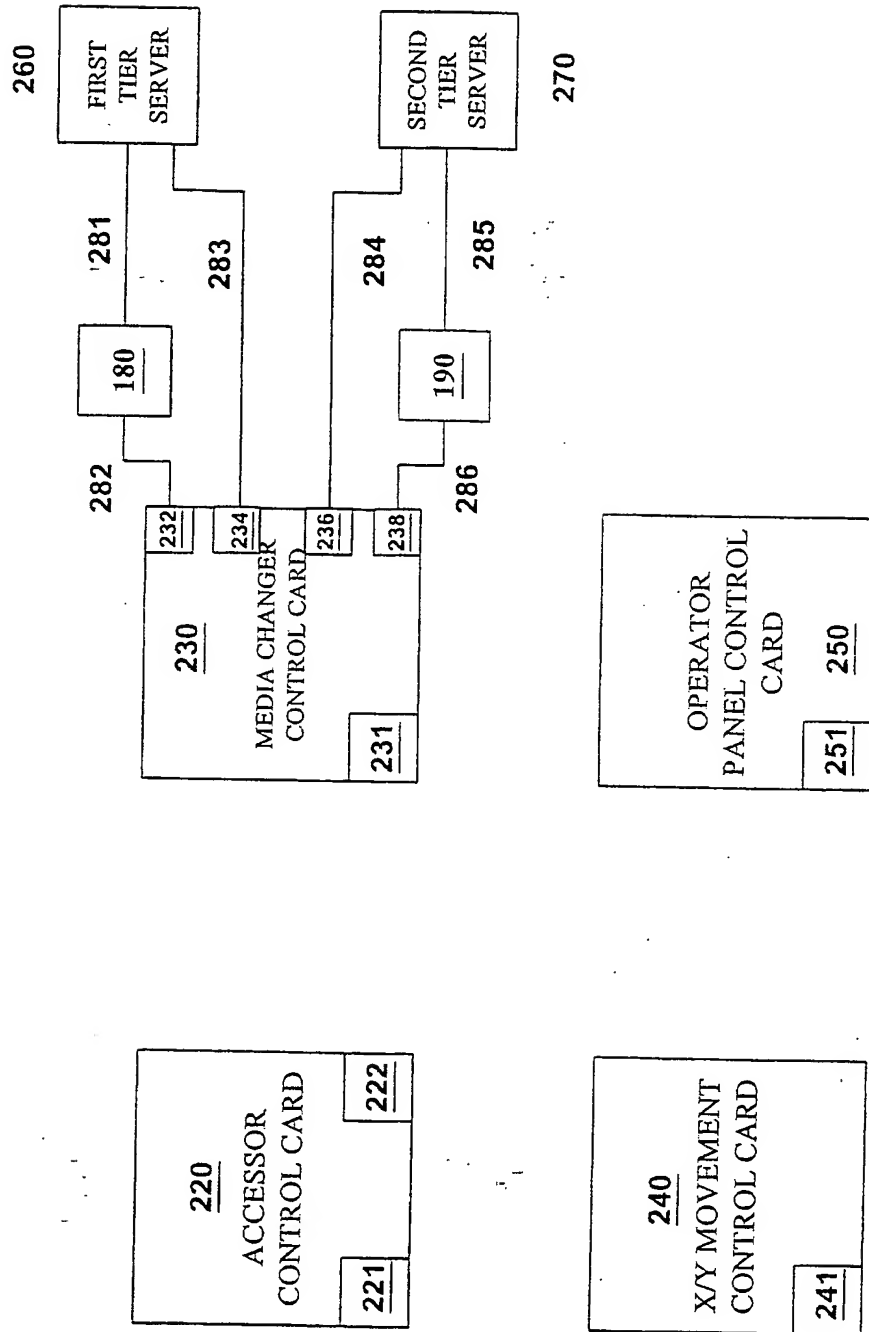


FIG. 2C



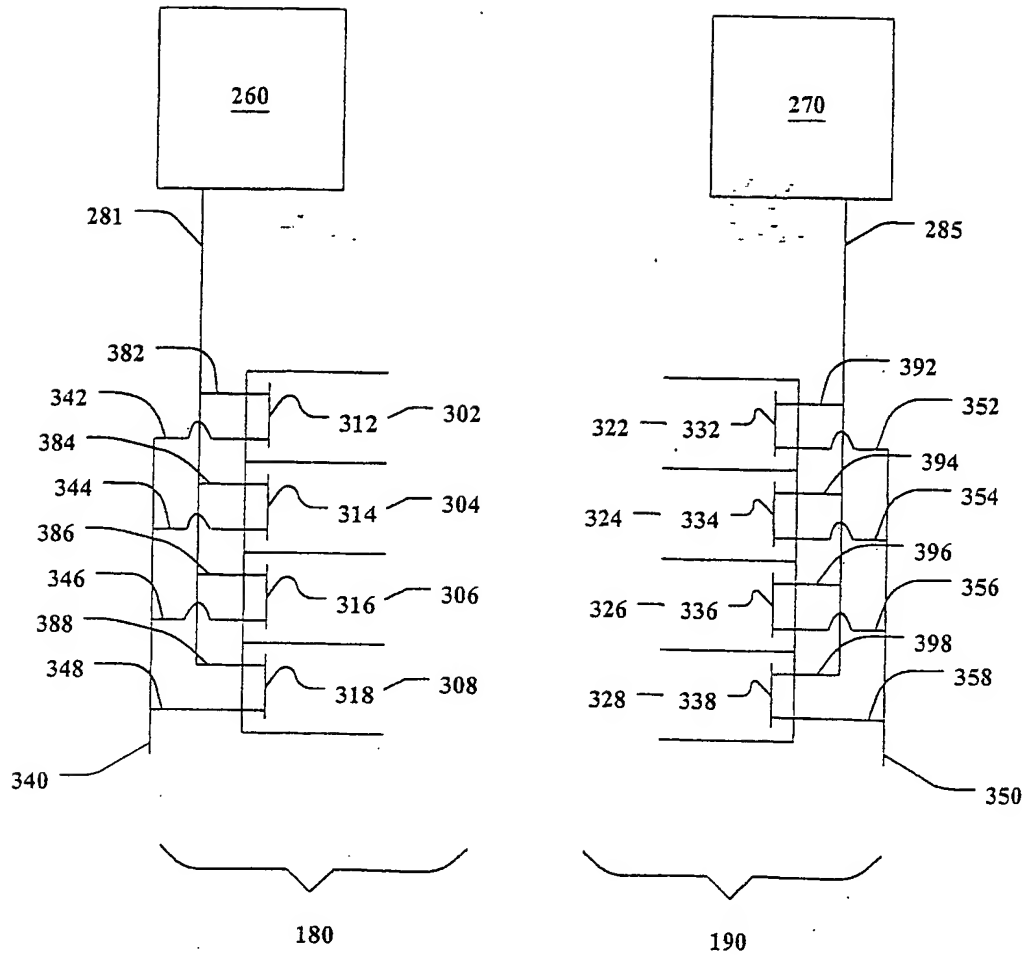


FIG. 3A

200220" 8496200T

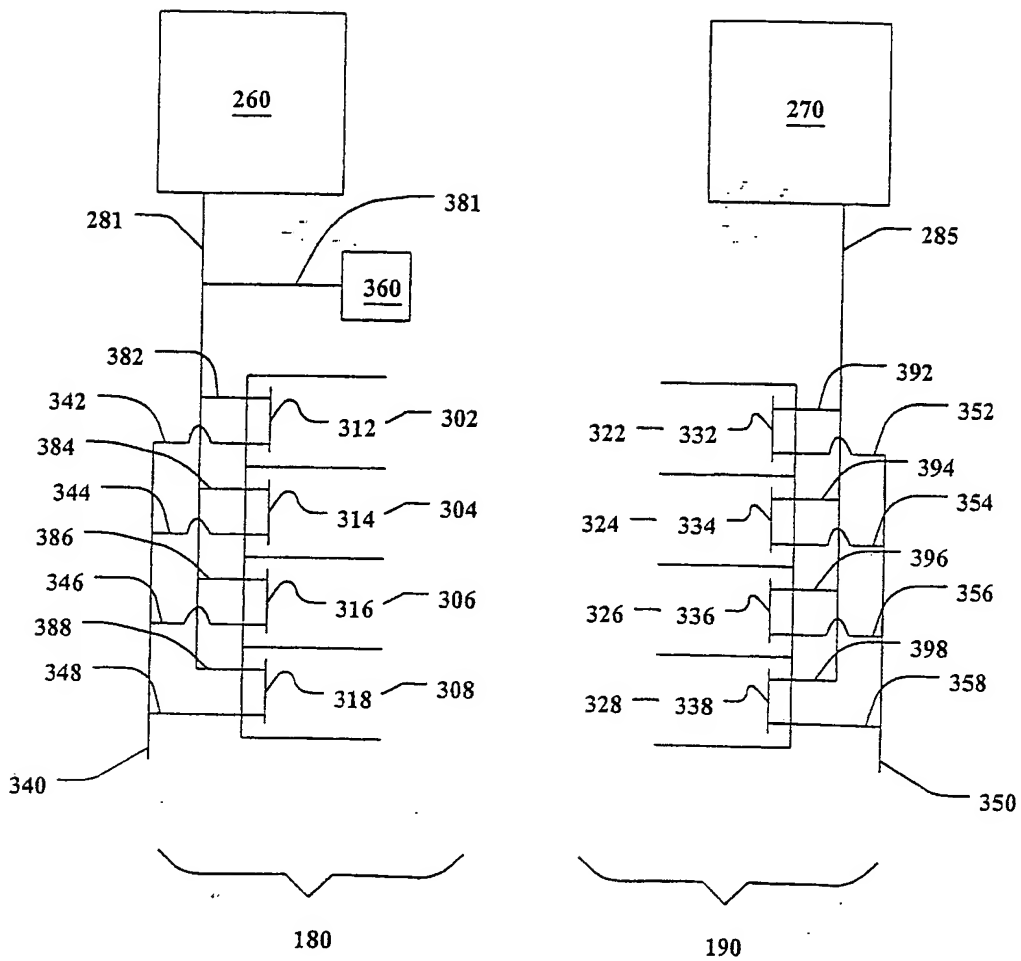


FIG. 3B

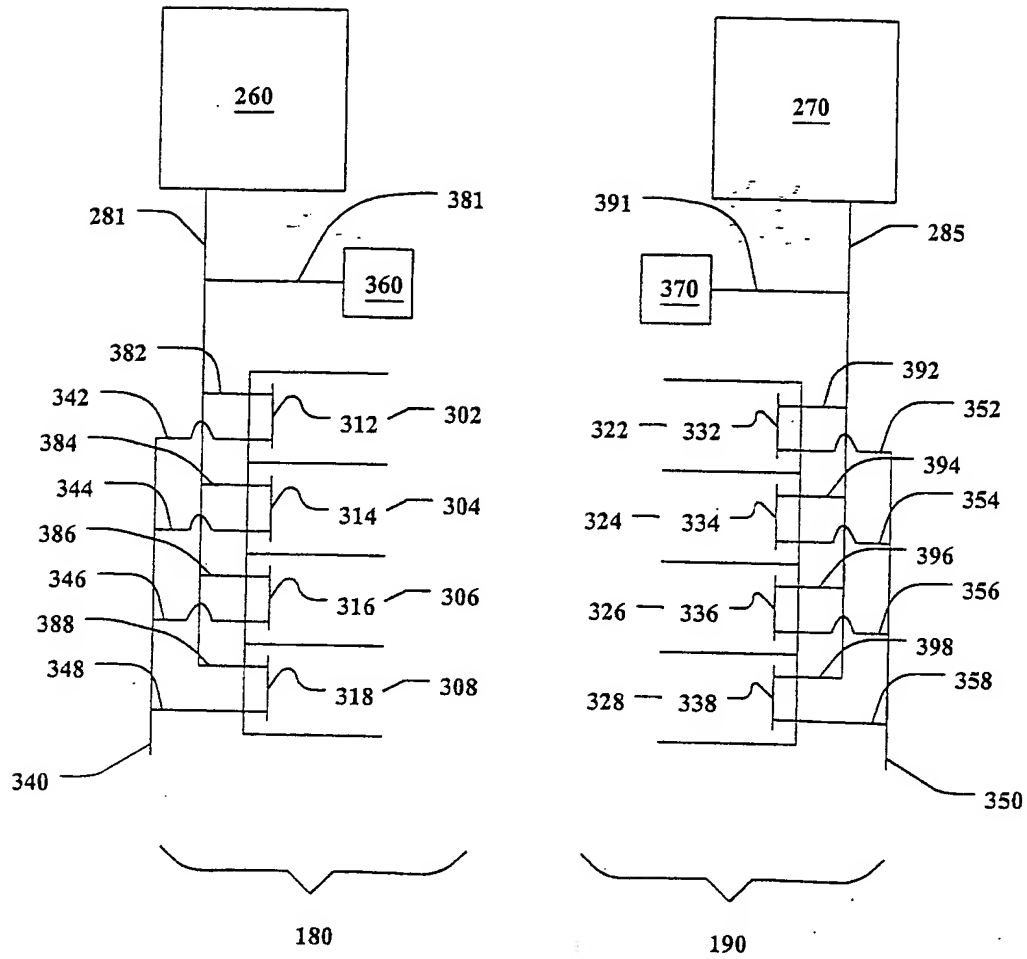
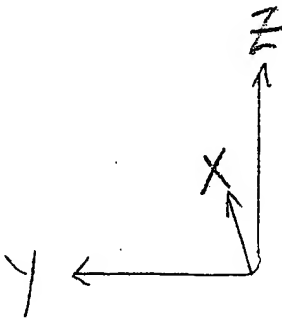
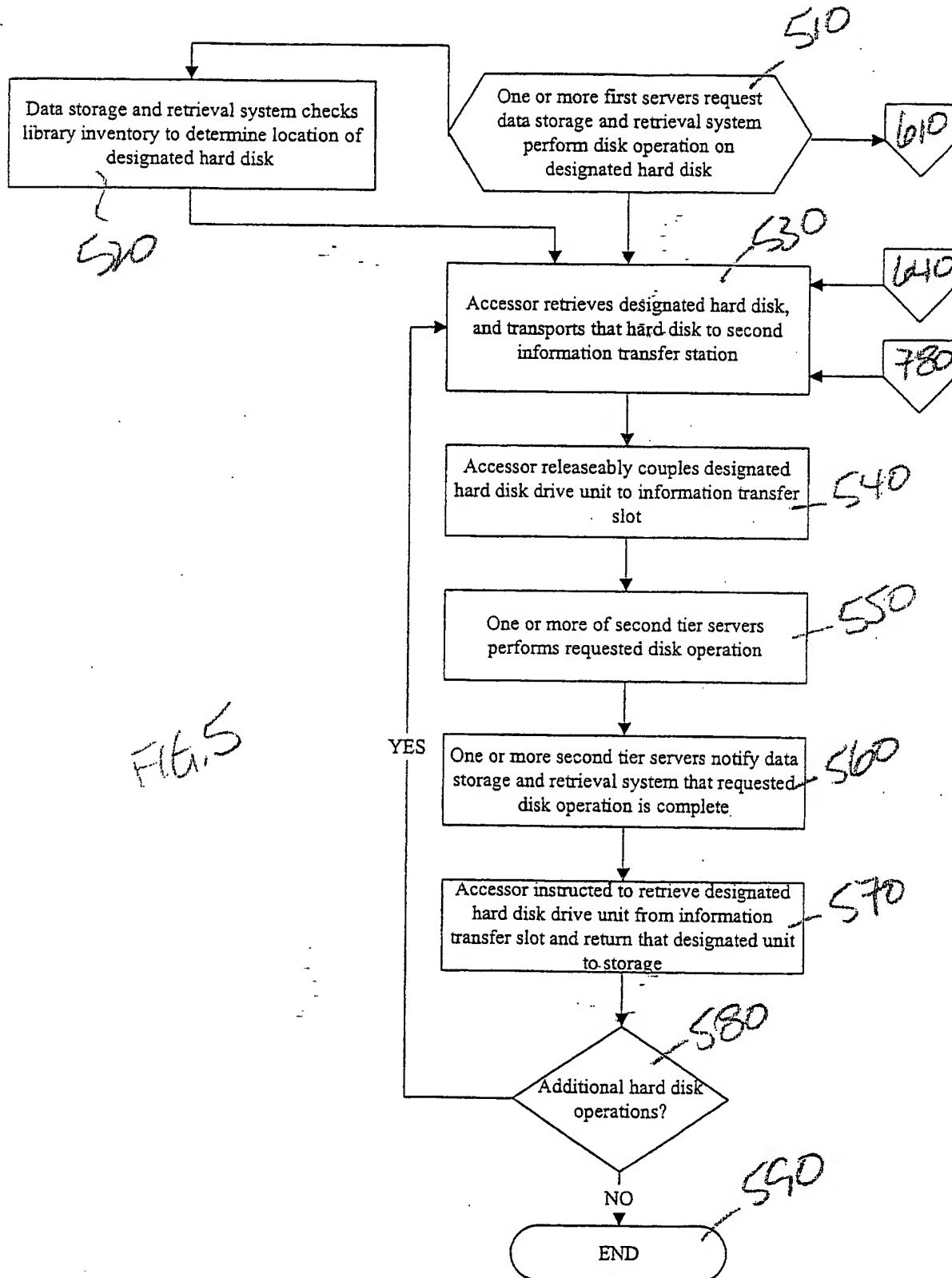


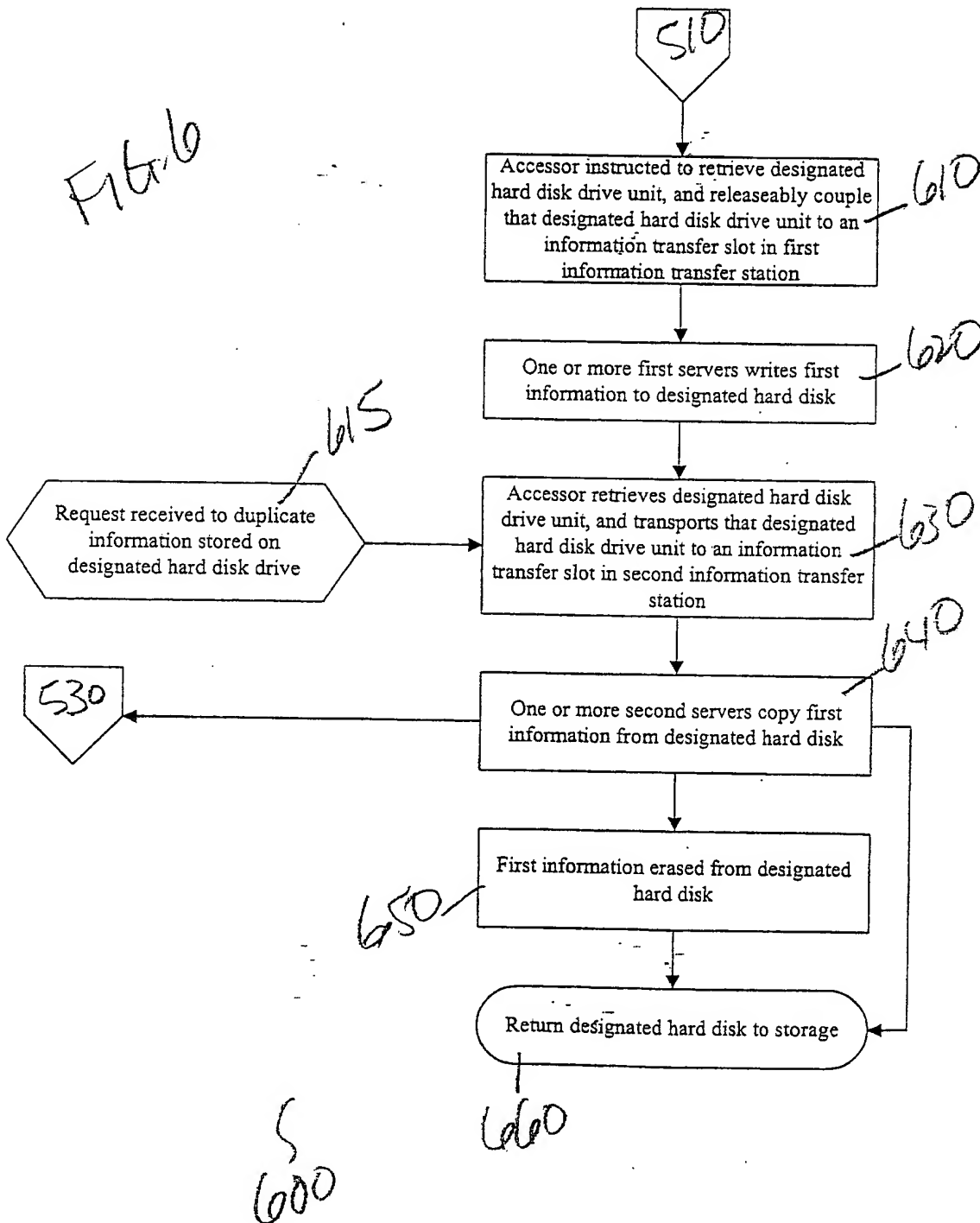
FIG. 3C

[illegible]



10079643-022002

Fig. 6



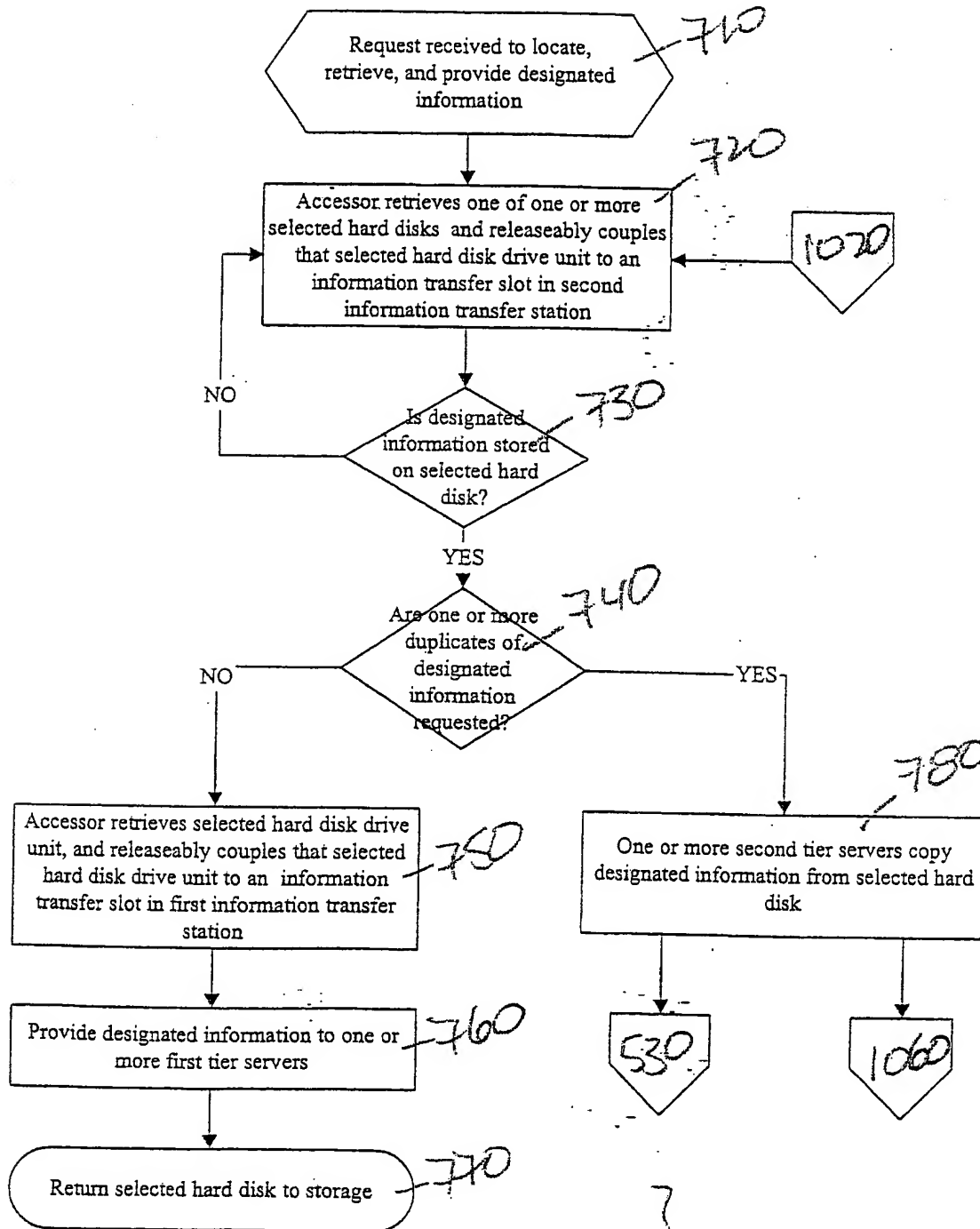


FIG. 7

7
700

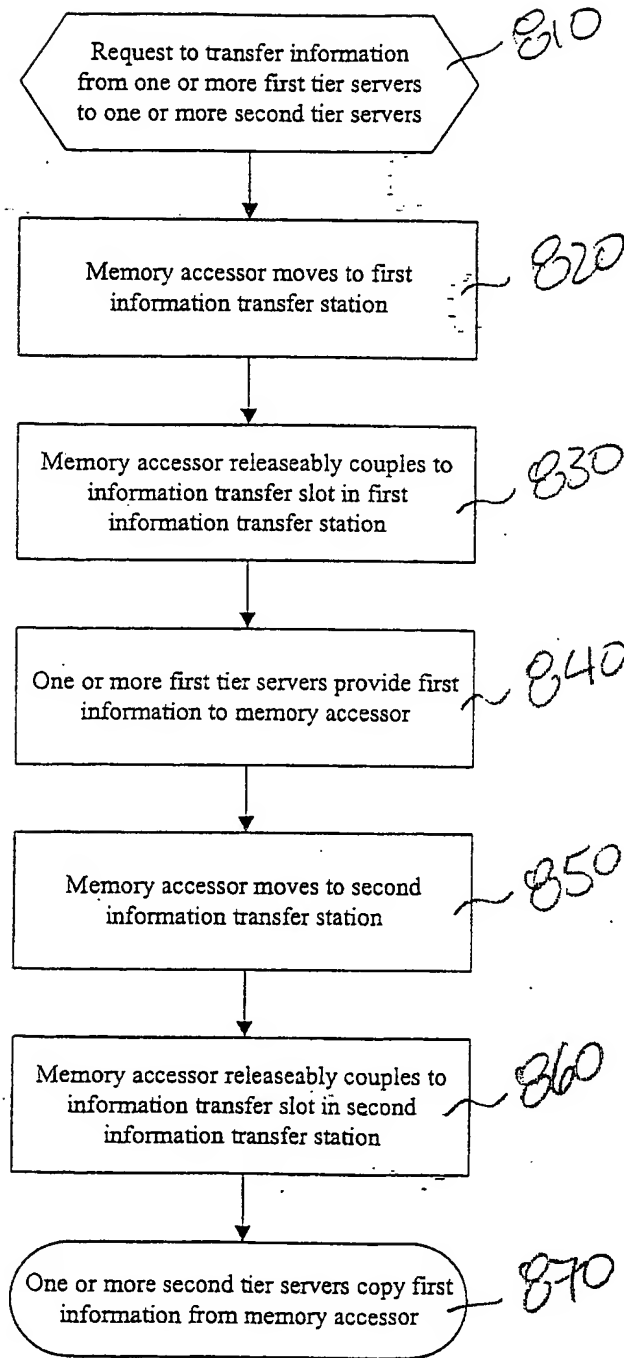
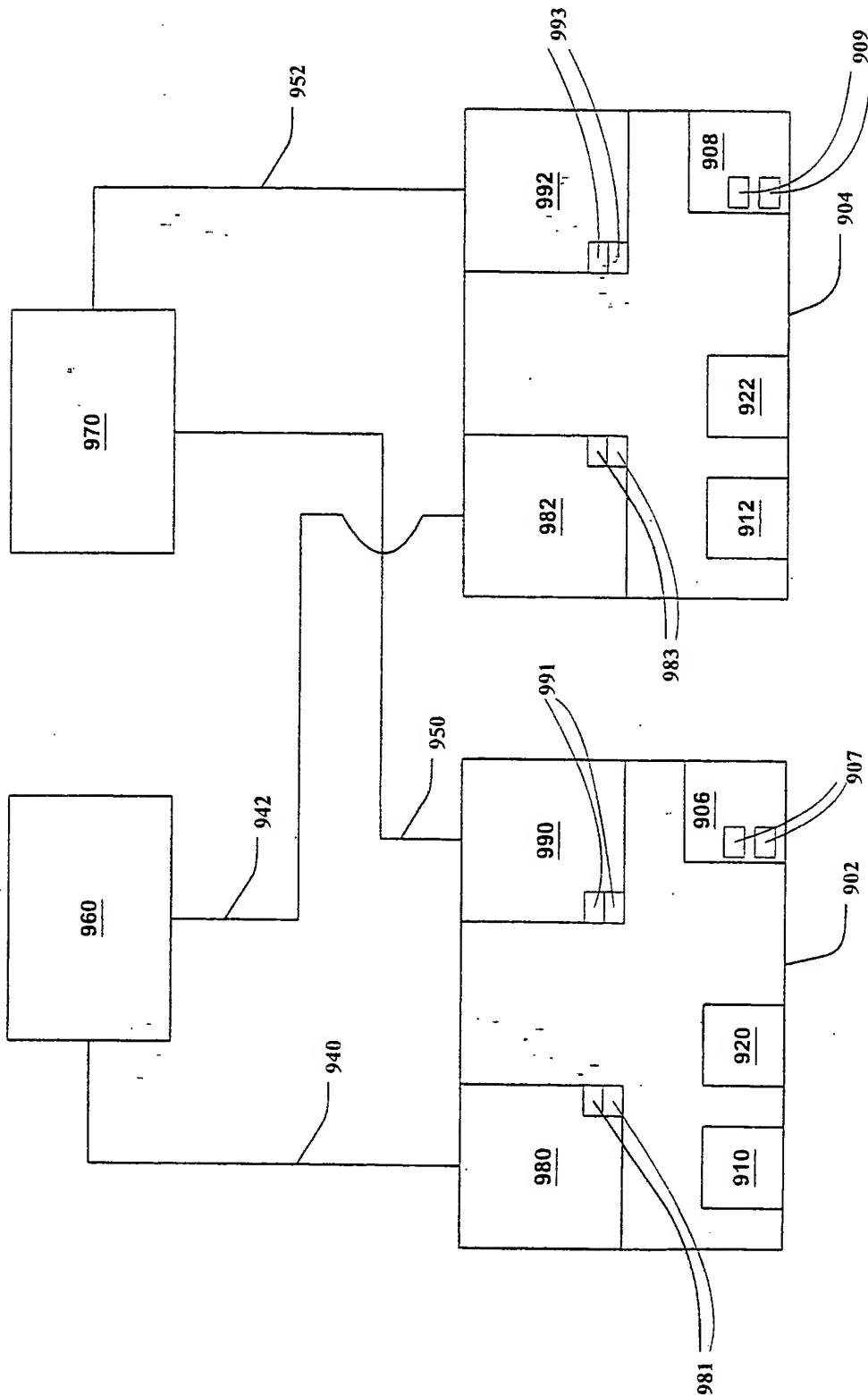


FIG. 8

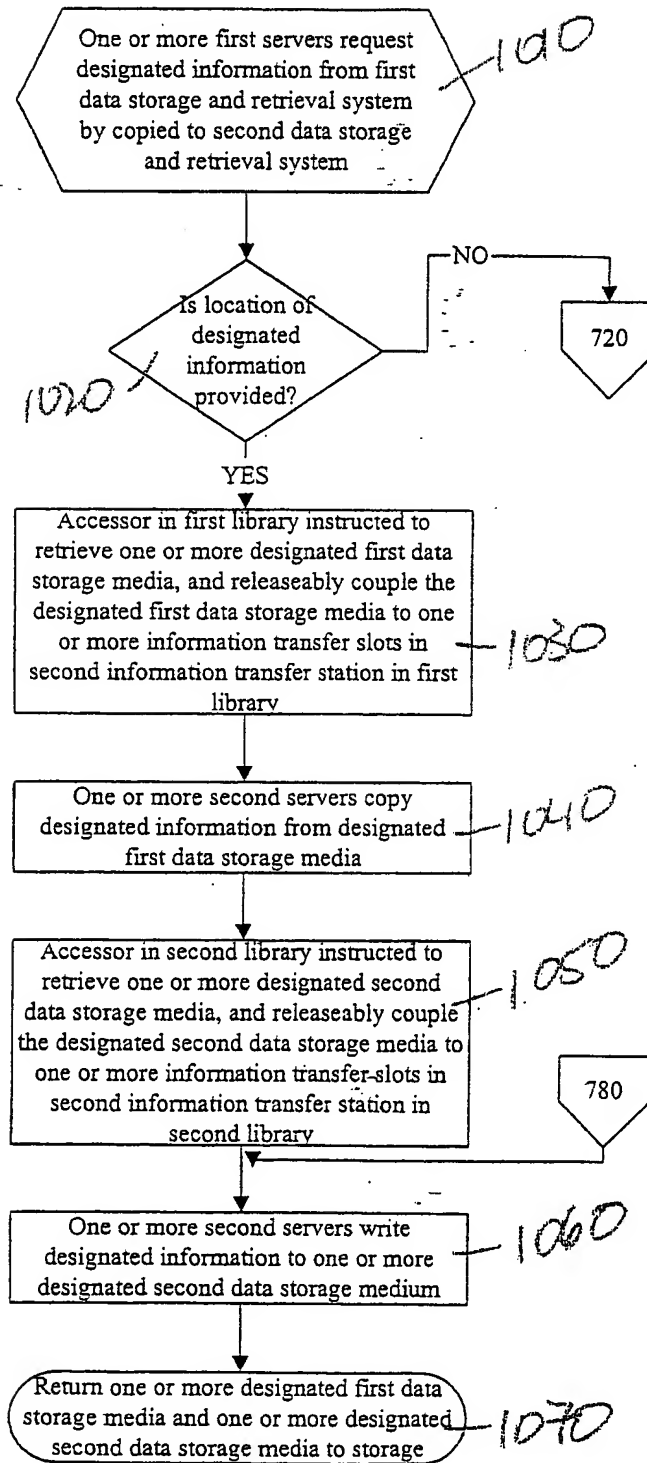
10079643 022002

FIG. 9A



The diagram illustrates a system architecture with two main processing units, 960 and 970, connected to a central bus 944. Unit 960 contains components 980, 990, 910, 920, 906, and 907. Unit 970 contains components 982, 992, 912, 922, 908, and 909. A separate bus 954 connects unit 970 to a component 970.

FIG-10



10079643 022002